

NF L..

10/725315

Classification: 365/226.000

Examiner: HUR, JOHN

GAU: 2824

Status: 30 - DOCKETED NEW CASE

Inventor: GRAAFF, SCOTT, et al

READY FOR EXAMINATION

Title: LOW POWER CONTROL CIRCUIT AND METHOD FOR A MEMORY DEVICE

Bib Data report

**Application Title: LOW POWER CONTROL CIRCUIT AND  
METHOD FOR A MEMORY DEVICE**

**Application Num:**  (in phx) 10/725315 **Filing Date:**  
12/02/2003 **Effective Filing:** 12/02/2003  
**(Location History)** **Foreign/Continuity Data)**

**Status: 30/DOCKETED NEW CASE - READY FOR  
EXAMINATION Status Date: 03/17/2004**

**Patent Number:** Not Issued **Issue Date:** N/A **Date of  
Abandonment:** N/A  
**Confirmation Number:** 2707 **PALM Location:**

**Examiner:** 79441 HUR, JOHN (Assignment Data)  
**Group Art Unit:** 2824 **Class/Subclass:** 365/226.000

**State or Country:** IDAHO **Sheets/Drawing:** 5 **Total  
Claims:** 76 **Independent Claims:** 8

**Inventors:**

Last name, First  
name:

GRAAFF, SCOTT

City:

BOISE

Country or  
State:

IDAHO

Q15E

10/725315

Examiner: HUR, JOHN

GAU: 2824

Classification: 365/226.000

Inventor: GRAAFF, SCOTT, et al

Status: 30 - DOCKETED NEW CASE - READY FOR EXAMINATION

Title: LOW POWER CONTROL CIRCUIT AND METHOD FOR A MEMORY DEVICE

Bib Data report

COWLES, TIM

PUISE

IDAHO

Attorneys: ALL Attorney Docket No: M4065.1001/P1001

Interference No: Lost Case: No Unmatched Petition:  
No L&R Code: 1